Serial No.: Unknown

Filed:

Herewith

Continuation of Serial No. 09/161,970

Continuation Filed: Sept. 28, 1998

Single Wafer Load Lock With Internal Wafer Transport

**Assistant Commissioner of Patents** Washington, D.C. 20231

Dear Sir:

Group Art Unit:

Unknown

Examiner:

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Unknown

## **CERTIFICATION UNDER 37 CFR 1.10**

I hereby certify that this correspondence is being deposited with the United States Postal Service on this date, , in an envelope as "Express Mail Post Office to Addressee," mailing label no. EL704518435US, addressed to: Assistant Commissioner for PATENT Patents, Box APPLICATION, Washington, D.C. 20231.

## PRELIMINARY AMENDMENT

Prior to examination of the above referenced application, the Applicant requests that the application be amended as follows:

In the claims:

Please carcel claims 1-45, without prejudice, and add the following claims:

- (New) A semiconductor processing system, comprising: 46.
  - a pod loader;
  - a mini-environment having a robot disposed therein;
  - one or more load lock chambers connected to the mini-environment; and
- one or more process chambers connected to the one or more load lock chambers, wherein each load lock chamber is connected to a single process chamber.
- 47. (New) The system of claim 46, wherein each load lock chamber comprises: an enclosure having a bottom, a lid and sidewalls defining a chamber cavity; and